

Don Bosco Institute of Technology

Department of Electronics and Telecommunication Engineering

SE, TE & BE Internal Assessment – II Timetable (AY 2022-23 ODD Semester)

S.E.(EXTC) - SEM III			
Date	Time of Commencement	Course Code & Name	Syllabus
17 October 2022	10:00 am to 11:00 am	ECC301 Engineering Mathematics - III	Module 2:ILT, Module 5: matrix theory, Module: vector differentiation 6.1,6.2, Module: complex variables 4.1,4.2, 4.3
18 October 2022	10:00 am to 11:00 am	ECC304 Network Theory	Module 3 Time & Frequency Domain Analysis & Module 5 Two Port Networks
19 October 2022	10:00 am to 11:00 am	ECC303 Digital System Design	Module 3.2, Module 4, Module 5
20 October 2022	10:00 am to 11:00 am	ECC302 Electronic Devices and Circuits	Module 2.1: Q point and regions of operations, Analysis and design of biasing circuits for BJT (Fixed bias & Voltage divider Bias). Module 2.2. Q point and region of operation for MOSFETs. Analysis and design of biasing circuits for JFET (self bias and voltage divider bias), E-MOSFET (Drain to Gate bias & voltage divider bias).
21 October 2022	10:00 am to 11:00 am	ECC305 Electronic Instrumentation & Control Systems	Module 3.3: Block diagram reduction techniques and Signal flow graph Module 4: Response of control system Module 5: Concept of stability: Routh and Hurwitz stability criterion
T.E (EXTC) - SEM V			
17 October 2022	10:00 am to 11:00 am	ECC504 Random Signal Analysis	Module 3.3, Module 4, Module 5
18 October 2022	10:00 am to 11:00 am	ECC501 Digital Communication	Module 3- LBC, Cyclic Codes, Convolution Codes Module 6- ASK, FSK, PSK
19 October 2022	10:00 am to 11:00 am	ECC502 Discrete Time Signal Processing	Module 2 and Module 3
20 October 2022	10:00 am to 11:00 am	ECCDLO5012 Data Compression and Cryptography	Module 3, Module 4, Module 5
	10:00 am to 11:00 am	Sensor Technology	Module 3, Module 4
	10:00 am to 11:00 am	Data Structures & Algorithms	Module 3, 4, 5 and 6
21 October 2022	10:00 am to 11:00 am	ECC503 Digital VLSI	Module 2.1: CMOS inverter operation, Voltage Transfer characteristics (VTC), Noise Margins, Propagation Delay, Power Dissipation, Design of CMOS Inverter, Layout of CMOS Inverter Module 2.2: Realization of CMOS NAND gate, NOR gate, Complex CMOS Logic Circuits, Layout of CMOS NAND, NOR and complex CMOS circuits
B.E. (EXTC) - SEM VII			
17 October 2022	10:00 am to 11:00 am	ECC701 Microwave Engineering	Module 2 Waveguides and Passive Devices Module 3 Microwave Tubes
18 October 2022	10:00 am to 11:00 am	ECCDLO7012 Deep Learning	Module 2 and Module 4
19 October 2022	10:00 am to 11:00 am	ECC702 Mobile Communication System	Module 2.2, Module 3, Module 4
20 October 2022	10:00 am to 11:00 am	ECCDLO7033 Internet Communication Engineering	Module 3, Module 4
21 October 2022	10:00 am to 11:00 am	ECC7016 Cyber Security and Laws	Module 4, Module 5 & Module 6

Dr. Ashwini Kotrashetti
IA Coordinator



PRINCIPAL

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